

Education System for TV Monitors Adjustment with I2C Serial Bus Interface

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Abstract — Television receivers and monitors have the possibilities to adjustment using built-in microcontrollers and I2C serial bus interface connected to each of the integrated circuits in the receiver or monitor. All parameters for this adjustment are stored in a suitable nonvolatile memory. It is very interesting for the students education to have the possibilities to control this process of adjustment, made this manually and do the appropriate measurements. The goal of this article is to develop an education system, which is capable to do these adjustments and measurements in the time of practical students works. Also it is possible to use these systems as a tool for monitor or receiver repairmen or testaments. It is possible to combine these adjustments and measurements with some visual observations of displayed testing signals and data on the monitor screen of the computer. All that can be made with the proposed tool, which is connected to a computer with a serial interface.

Keywords – TV Monitors Adjustment, I2C Interface, TV Education Systems

I. INTRODUCTION

The modern TV monitors and TV receivers are made with possibilities to automatically adjustment of their parameters, for example bright, contrast, colors, geometry, size, position, horizontal and vertical frequencies in dependence from the input horizontal and vertical synchronization pulses etc. [1] This is done with implementation of programmable integrated circuits, using a built in the monitor or receiver microcomputer and a memory for keep these values.

It is very interesting and important for education purposes to have the possibilities to control this process of adjustment, made this manually and do the appropriate measurements of the defined horizontal and vertical frequencies, when the monitor or receiver can be repaired or tested. Of course, it is possible to combine these measurements with some visual observations of displayed testing signals and data on the monitor screen of the computer. All that can be made and proposed as a tool, which is connected to a computer with a serial interface.

In the Fig.1 is shown the proposed block schema of the education system for adjustment and measurement of television received and monitors.

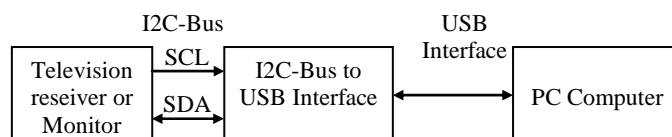


Fig.1. Block schema of the education system for adjustment and measurement of television received and monitors.

The television receiver or monitors, which are under the adjustment, measurement or testing for an education purpose have built-in I2C-Bus [2]. This bus is connected internally to almost all integrated circuits in the receiver or monitor. By means of this bus the built-in microcontroller, transmit data for control and adjustment of the receiver or monitor characteristics or modes of operation.

It is possible to accomplish a connection to this internal for television receiver or monitor I2C-bus, because most of them have a built-in connector for this bus. This connection to one external I2C-Bus to USB Interface is shown in the Fig.1. Of course, there is other possibly to interface I2C-Bus not only with USB to one PC computer, for example Serial Interface RS232, Parallel Port etc., but the USB Interface is more effective and suitable for interfacing with PC computers for software developments and education purposes.

In the monitors there is a standard testing socket utilized to connect an external interface. This socket is shown in Fig.2.

Except of I2C-Bus signals SCL_OUT and SDA_IN/OUT in this socket are included also horizontal HSYNC_IN and vertical VSYNC_IN input signals SELECT_IN. This unification of the pins and signals is very convenient for testing different types of monitors with the same type of an external interface to PC computer.

There are many possibilities to choose the hardware of the I2C-Bus to USB Interface: a specific only for this application designed interface or to use universal I2C-Bus to USB interface, with corresponding software for PC computer. From a point of view of education application it is more suitable to choose and utilize universal interface. Moreover, it can be used too in other students practical work, tutorials etc.

II. THE EDUCATION SYSTEM FOR TV MONITORS

In this work it is chosen the popular for education and professional applications hardware and software measurement system of National Semiconductors known as LabVIEW [3]. This interface is NI USB-8451 [3] and it can support both I2C-Bus and serial peripheral interface (SPI), which too is very popular as I2C-Bus in television receivers and monitor internal bus for adjustment and control with microcontrollers.

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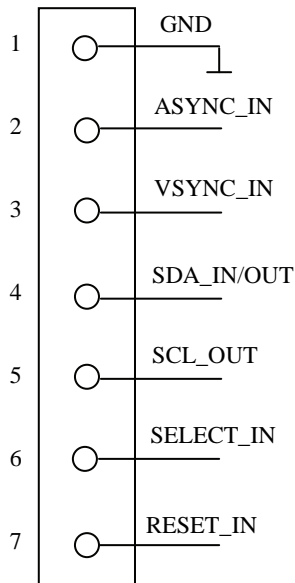


Fig.2. The pins assignment of the standard testing socket utilized in monitors

The main characteristic of this interface is:

- I2C master interface with clock rates up to 250 kHz;
- 8 general purpose digital I/O lines;
- high level, easy-to-use LabVIEW API;
- bus powered, full-speed (12 Mb/s) USB connectivity;
- 7 and 10-bit I2C addressing;
- advanced API for custom I2C and SPI transactions;
- Windows 2000/XP operating system.

III. THE UTILISATION OF EDUCATION SYSTEM

The chosen LabVIEW I2C-Bus to USB interface can be used in the proposed education system for practical works of the students with block schema described in Fig.1.

In the beginning of the practical work students build itself a block diagram used in common LabVIEW applications, which in the other hand contain the specific building block suitable for the application of television receiver or monitor testing and adjustment with proposed I2C – Bus interface. This block diagram is shown in Fig.3.

The blocks for settings of some necessary data are shown in Fig.3 are:

- Data In using for data input and transfer via I2C – Bus;
- I2C Address also needed for addressing the transferred via I2C – Bus data;
- Board Type is needed for a chosen type of I2C – Bus to USB interface;
- Serial Number also is necessary for board type identification.

There are also the execution blocks:

- I2C Open for activating I2C - Bus;
- I2C Tx/Rx for transferring (Write/Read) operation for the data;
- I2C Close for deactivating I2C – Bus connected to the television receiver or monitor.

At the end of this block diagram is added a block for data visualization for example of errors, which is named Error Out.

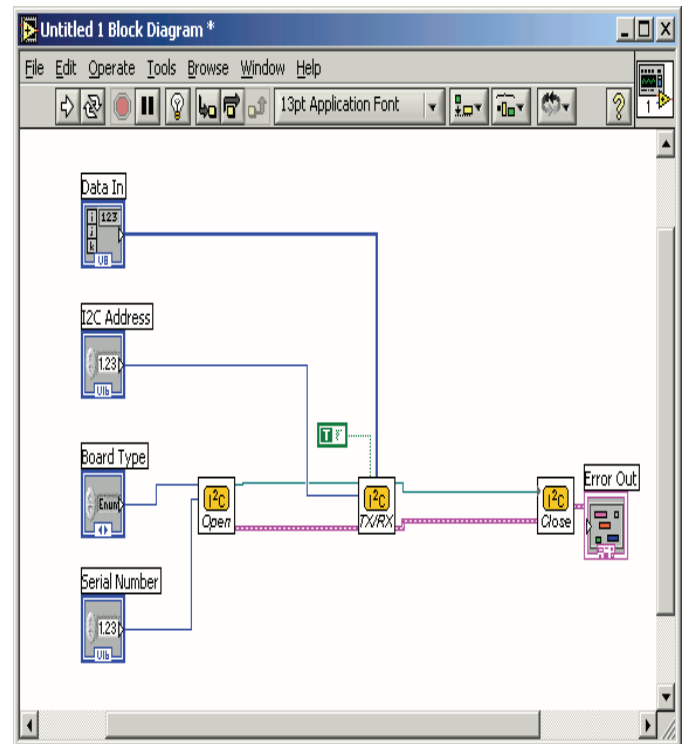


Fig.3. Block diagram in LabVIEW applications for I2C data transferring

In Fig.4 are shown some fields and locations in front panel, where it is possible to enter some initial data for Board Type, Serial number.

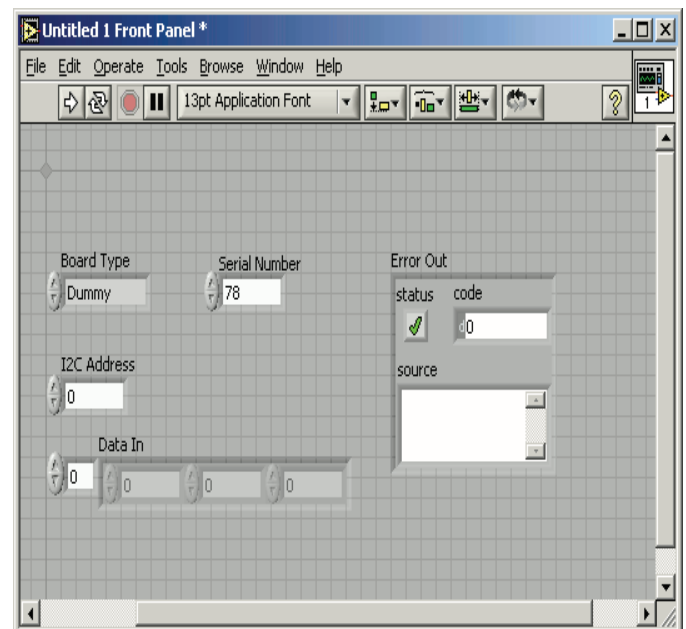


Fig.4. The window with some fields and locations in front panel, where it is possible to enter some initial data for Board Type, Serial number, Address, Data In and seen Errors Out

Also there are the locations for settings of I2C Address and data which are sending via I2C-bus to the television receiver or monitor and especially to the desired integrated circuit in them, where it is necessary to make an adjustment or change of the parameters or mode to operation of the television receiver or monitor.

The software possibilities to examine, testing and adjust the television receivers and monitors are very suitable to perform a varieties of the students practical works mainly to show them all important points and moments of the adjustment or testing process of one television receiver or monitor. These important moments or points are for example the time diagrams for data transferring for PC computer to the adjusted or tested television receiver or monitor, the contents of some important registers or memory cells in the integrated circuits in receiver or monitor, from which the students can understand more clear and deeply the work and adjustment of this part, from which depend the mode of working of the television receiver or monitor.

Some of these and other important moments for proposed education system for television receivers or monitors adjustment are shown in the next figures as windows on the PC computer screen.

It is show in the Fig. 5 the time diagram for an example of transferring data via I2C-bus.

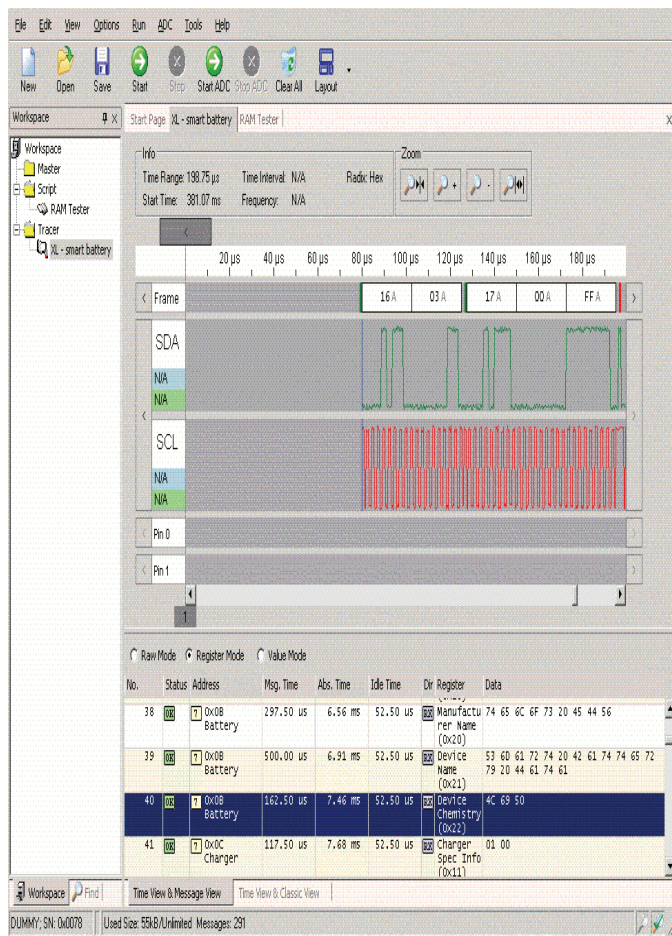


Fig.5. Time diagram of data bus SDA of I2C interface is added also in the window

It can be seen from the students the sequence of the data in the data bus SDA of I2C – bus together or in synchronization with I2C – bus clock signal SCL. Also it is possible to measure the time relations of two wire serial I2C – bus, which are presented as a scale in microseconds above the time sequences for SDA and SCL signals.

A more clear representation of the time diagram of data bus SDA of I2C interface is added also in the window and is shown on the Fig.5 as hex values of a sequence of eight bits or one data byte. This is the line above time diagram for data bus SDA of I2C interface and it is labeled as Frame in Fig. 5. This give to the students the possibility to do the comparison for the data they are set and the data, which are transmit and also the possibility to test whether or not the transmit data are received correctly in the desired integrated circuit under the adjustment in the television receiver or monitor.

Below the time diagrams there in the Fig. 5 are the additional parts of the window, which too are very informative and useful and some of which are presented as values in microseconds and other as hex values.

For some other practical works with students in the area of television receivers or monitors testing and adjustments it is more advisable to have the possibilities of observation not only the transferring process via the I2C interface, but also the contents of some of the internal registers in the integrated circuits in the television receiver and monitor or the contents of some memory cells of EEPROM also in television receiver and monitor. This possibility is included in the described education system. In Fig.6. is shown a window in which it can be seen the contents of some memory cells.

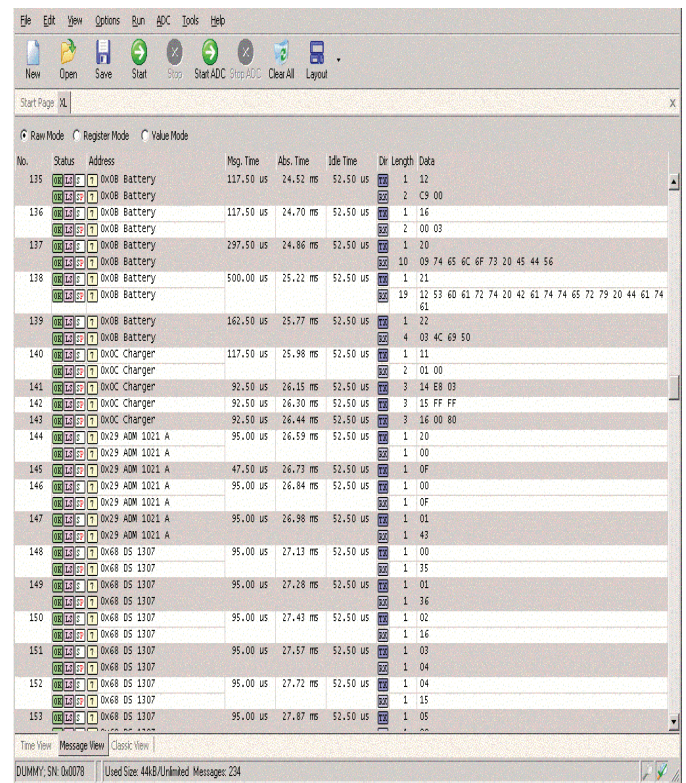


Fig.6. Window for observation of the contents of some of some memory cells of EEPROM

In the Fig.6 it can be seen a list of memory cells with their status, address, contents, direction of data transfer labeled as Tx/Rx and the appropriate message, absolute, idle time and length.

The similar representation of internal register of the integrated circuits in the television receivers or monitors representation is given in Fig.7.

No.	Status	Address	Msg. Time	Abs. Time	Idle Time	Dir	Register	Data
135	0x08 Battery	117.50 us	24.52 ms	\$2.50 us		0x00	Average Time To Empty (0x12)	C9 00
136	0x08 Battery	117.50 us	24.70 ms	\$2.50 us		0x03	Battery Status (0x16)	00 03
137	0x08 Battery	297.50 us	24.86 ms	\$2.50 us		0x00	Manufacturer Name (0x20)	74 65 6C 6F 73 20 45 44 56
138	0x08 Battery	500.00 us	25.22 ms	\$2.50 us		0x01	Device Name (0x21)	53 6D 61 72 74 20 42 61 74 74 65
139	0x08 Battery	162.50 us	25.77 ms	\$2.50 us		0x02	Device Chemistry (0x22)	4C 69 50
140	0x0C Charger	117.50 us	25.98 ms	\$2.50 us		0x01	Charger Spec Info (0x11)	01 00
141	0x0C Charger	92.50 us	26.15 ms	\$2.50 us		0x03	Charging Current (0x14)	E8 03
142	0x0C Charger	92.50 us	26.30 ms	\$2.50 us		0x05	Charging Voltage (0x15)	FF FF
143	0x0C Charger	92.50 us	26.44 ms	\$2.50 us		0x06	Alarm Warning (0x16)	00 00
144	0x29 ADM 1021 A	95.00 us	26.58 ms	\$2.50 us		0x0F	Register Pointer: Invalid	
145	0x29 ADM 1021 A	47.50 us	26.73 ms	\$2.50 us		0x0F	One-Shot (0x0F)	
146	0x29 ADM 1021 A	95.00 us	26.84 ms	\$2.50 us		0x00	Local Temp. Value (0x00)	0F
147	0x29 ADM 1021 A	95.00 us	26.98 ms	\$2.50 us		0x01	Remote Temp. Value (0x01)	43
148	0x68 DS 1307	95.00 us	27.13 ms	\$2.50 us		0x00	Seconds (0x00)	35
149	0x68 DS 1307	95.00 us	27.28 ms	\$2.50 us		0x01	Minutes (0x01)	36
150	0x68 DS 1307	95.00 us	27.43 ms	\$2.50 us		0x02	Hours (0x02)	16
151	0x68 DS 1307	95.00 us	27.57 ms	\$2.50 us		0x03	Day (0x03)	04
152	0x68 DS 1307	95.00 us	27.72 ms	\$2.50 us		0x04	Date (0x04)	15
153	0x68 DS 1307	95.00 us	27.87 ms	\$2.50 us		0x05	Month (0x05)	09
154	0x68 DS 1307	95.00 us	28.02 ms	\$2.50 us		0x06	Year (0x06)	05
155	0x68 DS 1307	70.00 us	28.16 ms	\$2.50 us		0x06	Year (0x06)	FF
156	0x08 Battery	117.50 us	28.41 ms	\$2.50 us		0x03	Battery Mode (0x03)	00 FF
157	0x08 Battery	117.50 us	28.58 ms	\$2.50 us		0x08	Voltage (0x08)	10 27
158	0x08 Battery	117.50 us	28.75 ms	\$2.50 us		0x04	Current (0x04)	F4 01
159	0x08 Battery	117.50 us	28.92 ms	\$2.50 us		0x08	Average Current (0x08)	25 00
160	0x08 Battery	117.50 us	29.08 ms	\$2.50 us		0x11	Run Time To Empty (0x11)	64 00
161	0x08 Battery	117.50 us	29.26 ms	\$2.50 us		0x12	Average Time To Empty (0x12)	C9 00
162	0x08 Battery	117.50 us	29.43 ms	\$2.50 us		0x16	Battery Status (0x16)	00 03
163	0x08 Battery	297.50 us	29.60 ms	\$2.50 us		0x20	Manufacturer Name (0x20)	74 65 6C 6F 73 20 45 44 56
164	0x08 Battery	500.00 us	29.95 ms	\$2.50 us		0x21	Device Name (0x21)	53 6D 61 72 74 20 42 61 74 74 65
165	0x08 Battery	162.50 us	30.50 ms	\$2.50 us		0x22	Device Chemistry (0x22)	4C 69 50
166	0x0C Charger	117.50 us	30.72 ms	\$2.50 us		0x11	Charger Spec Info (0x11)	01 00
167	0x0C Charger	92.50 us	30.89 ms	\$2.50 us		0x14	Charging Current (0x14)	E8 03

Fig.7. Window for observation of the contents of some of the internal registers in the integrated circuits

Also in Fig.8 there is a window in which can be seen the representation of some values of logical data or variables.

No.	Status	Address	Msg. Time	Abs. Time	Idle Time	Dir	Value	Registers
135	0x08 Battery	117.50 us	24.52 ms	\$2.50 us		0x00	Average Time To Empty (0x12)	0x12 (0.0..1.7)
136	0x08 Battery	117.50 us	24.70 ms	\$2.50 us		0x03	Over Charged Alarm	0x14 (1.7)
137	0x08 Battery	297.50 us	24.86 ms	\$2.50 us		0x00	Terminate Charge Alarm	0x14 (1.8)
138	0x08 Battery	500.00 us	25.22 ms	\$2.50 us		0x03	Over Temp Alarm	0x14 (1.9)
139	0x08 Battery	162.50 us	25.77 ms	\$2.50 us		0x02	Temperature Drop Into Acceptable Range (0x02)	0x14 (1.9)
140	0x08 Battery	500.00 us	25.98 ms	\$2.50 us		0x01	Discharge Is No Longer Detected (0x01)	0x14 (1.9)
141	0x08 Battery	297.50 us	26.15 ms	\$2.50 us		0x00	Remaining Capacity Alarm	0x14 (1.1)
142	0x08 Battery	162.50 us	26.30 ms	\$2.50 us		0x03	Remaining Capacity Alarm - (0x03)	0x14 (1.1)
143	0x08 Battery	500.00 us	26.44 ms	\$2.50 us		0x06	Average Time To Empty - Remaining Time Alarm (0x06)	0x14 (1.0)
144	0x08 Battery	162.50 us	26.58 ms	\$2.50 us		0x0F	Initialized	0x14 (0.7)
145	0x08 Battery	47.50 us	26.73 ms	\$2.50 us		0x0F	Calibration or Configuration Information Has Been Lost and Accuracy is Significantly Degraded (0x0F)	0x14 (0.6)
146	0x08 Battery	95.00 us	26.84 ms	\$2.50 us		0x00	Battery is Accepting a Charge Current (0x00)	0x14 (0.6)
147	0x08 Battery	95.00 us	26.98 ms	\$2.50 us		0x01	Battery is No Longer Considered in a Full State (0x01)	0x14 (0.6)
148	0x08 Battery	95.00 us	27.13 ms	\$2.50 us		0x02	Relative State of Charge > 20% (0x02)	0x14 (0.6)
149	0x08 Battery	95.00 us	27.28 ms	\$2.50 us		0x03	Or (0x03)	0x14 (0.6..0.8)
150	0x08 Battery	95.00 us	27.43 ms	\$2.50 us		0x04	telos EDV	0x20 (0.6..0.7)
151	0x08 Battery	95.00 us	27.57 ms	\$2.50 us		0x05	Smart Battery Data	0x21 (0.6..1.7)
152	0x08 Battery	95.00 us	27.72 ms	\$2.50 us		0x06	LiP	0x21 (0.6..1.7)
153	0x08 Battery	95.00 us	27.87 ms	\$2.50 us		0x07	Version 1.0 (0x07)	0x21 (0.6..0.8)
154	0x08 Battery	95.00 us	28.02 ms	\$2.50 us		0x08	Selector Commands: Not Supported (0x08)	0x21 (0.8)
155	0x08 Battery	95.00 us	28.16 ms	\$2.50 us		0x09	1000 mA (0x09)	0x14 (0.6..1.7)
156	0x08 Battery	95.00 us	28.31 ms	\$2.50 us		0x0A	Constant Current AC Charging Current needed mV (0x0A)	0x14 (0.6..1.7)
157	0x08 Battery	95.00 us	28.46 ms	\$2.50 us		0x0B	Battery is Fully Charged and Charging is Complete (0x0B)	0x14 (1.7)

Fig.8. Window which can be seen the representation of some values of logical data or variables

A more difficult, but very important and suitable direction of applying of the proposed education system is not only in the practical works of the students, but also for the students projects. This means, that the students use this education system for development and running of their own projects as a concrete Windows applications written for example in C, C++, Visual C++, Visual Net, Java etc.

Such example for using the proposed education system for the purpose of student projects as application written in Visual C++ is shown in Fig.9.

```

MasterExample - Microsoft Visual C++ [design] - MasterExample.cpp [Read Only]
File Edit View Project Build Debug Tools Window Help
Start Page MasterExample.cpp
(Globals) MasterTransmitter

void MasterTransmitter (CMaster &master)
{
    // Create a vector object containing the data to be sent to
    // the slave (0..255).
    vector<unsigned char> data;

    for (int i=0; i < 256; i++)
        data.push_back (i);

    // Create an I2CAddress containing the address of the I2C slave
    // (7 bit address: 0x50).
    I2CAddress address (0x50, false);

    // Create a master transmitter object.
    CMasterMessageTx tx_msg (address, data);

    // Set the master bitrate to 100 kHz.
    master.SetBitrate (100000);

    // Transfer the data to the I2C slave.
    master.TransferData (tx_msg);

    // Display the number of bytes transferred to the slave.
    cout << "Transmitted " << tx_msg.GetBytesTransferred()
        << " bytes successfully as master transmitter." << endl;
}

```

Fig.9. Example of using the education system as students projects application written in Visual C++

IV. CONCLUSION

The proposed education system for TV receivers or monitors adjustments is realized as standard LabVIEW I2C to USB interface and with using almost existing software for data transferring and visualization, but it is open for student's projects and for other modifications, extensions or new releases.

REFERENCES

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