

Analysis of LCC resonant DC-DC converter with capacity filter in the load circuit

Aleksandar S. Vuchev¹, Nikolay D. Bankov² and Georgi P. Terziyski³

Abstract – A study of LCC resonant DC-DC converter with capacity filter in the load circuit is accomplished. To this end, a harmonic analysis is used. Expressions for determination of main quantities of the converter are obtained. The areas of commutation of the power switches are determined at both zero current and zero voltage.

Keywords – LCC resonant DC-DC converter.

I. INTRODUCTION

Resonant converters are widely used in the building of power supply devices. It is largely due to their peculiar capacity for soft commutation of the power devices. Converters with second order resonant circuits are the most widespread among them and their resonant circuit comprises only an inductor and a capacitor. When their operating frequency is lower than the resonant one, the controllable switches commutate at zero current, i.e., Zero Current Switching (ZCS) occurs. Conversely, when the operating frequency is higher than the resonant one, this results in Zero Voltage Switching (ZVS). For obtaining better power features it is preferred to have the operating frequency higher than the resonant one.

Despite the great number of advantages, the converters with second order resonant circuits also reveal a considerable drawback – their disability to work in the whole range from a no-load to a short circuit state retaining at the same time the conditions of soft commutation of the controllable switches. This limitation can be overcome by the use of a higher order resonant circuit. Very good results are obtained if just one more reactive element – an inductor or a capacitor – is added to the circuit [1].

LCC converters have been lately used in building arc welders [2], power supplies of fluorescent lamps [3], lasers, etc. In this case, the load circuit behaves like a rectifier with a capacitive filter with respect to the resonant inverter.

Very often in theoretical studies of resonant converters harmonic analysis is applied. Besides, to obtain results with

¹Aleksandar S. Vuchev is with the Faculty of Electrical Engineering and Electronic, 26 Maritza st., 4002 Plovdiv, Bulgaria, E-mail: avuchev@yahoo.com

²Nikolay D. Bankov is with the Faculty of Electrical Engineering and Electronic, 26 Maritza st., 4002 Plovdiv, Bulgaria, E-mail: nikolay_bankov@yahoo.com

³Georgi P. Terziyski is with the Faculty of Electrical Engineering and Electronic, 26 Maritza st., 4002 Plovdiv, Bulgaria, E-mail: g_terziyski@yahoo.com

reasonable accuracy, the influence of the first harmonics of the currents and voltages are only taken into account [4] – i.e. the "first harmonic analysis" method is typically used.

The purpose of this paper is studying an LCC resonant DC-DC converter with a capacitive filter in the load circuit by the first harmonic analysis. As a result of the analysis dependencies of basic quantities are to be obtained by which the limits of the converter efficiency while retaining soft commutation conditions of the controllable switches are to be defined.

II. ANALYSIS OF THE CONVERTER

A. General Assumptions

The circuit diagram of the converter under consideration is shown in Figure 1. It comprises an inverter (controllable switches $S_1 \div S_4$ with reverse diodes $D_1 \div D_4$), a resonant circuit

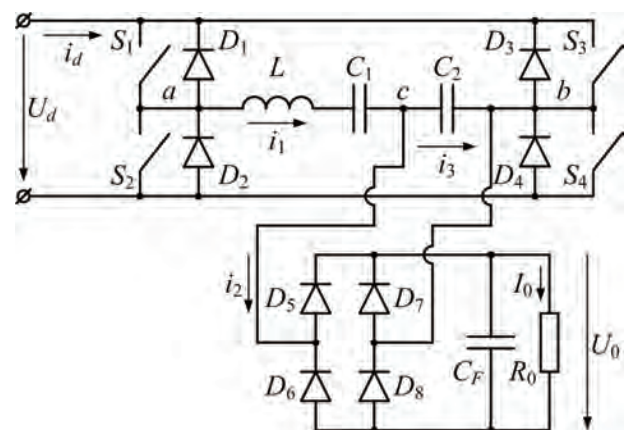


Fig.1. LCC resonant DC-DC converter with capacity load filter

(L , C_1 and C_2), an uncontrollable DC-DC converter ($D_5 \div D_8$), a capacitive filter (C_F), and a load resistor (R_0).

For the purposes of the analysis it is assumed that all elements in the circuit diagram are ideal (no losses in them), the power devices switch instantly and the pulsations of the power supply voltage U_d and the output voltage U_0 are negligible. It is also assumed that, according to the chosen analytical method, the first harmonics of the currents and voltages only act in the circuit under consideration.

In converters with third order resonant circuits two resonant frequencies are observed [5]. For the considered converter, they are defined as follows:

$$\omega_0 = 1/\sqrt{LC_1} \quad \omega_{01} = 1/\sqrt{LC_1 C_2 / (C_1 + C_2)}. \quad (1)$$

The lower resonant frequency (ω_0 here) is chosen to be the basic one.

In this study it is assumed that the operating frequency of the converter is higher than the basic resonant frequency ω_0 .

In accordance with the above, by analogy with the analyses of converters with second order resonant circuit, wave impedance and frequency distraction are determined:

$$\rho_0 = \sqrt{\frac{L}{C_1}} \quad v = \frac{\omega_s}{\omega_0}, \quad (2)$$

where the operating frequency is denoted by ω_s .

Using (2), for the impedances of the inductor and the capacitors of the resonant circuit it is obtained:

$$Z_L = jv\rho_0 \quad Z_{C1} = -j\frac{\rho_0}{v} \quad Z_{C2} = \frac{Z_{C1}}{a}, \quad (3)$$

where $a = C_2/C_1$ is the ratio between the capacitances of the two capacitors.

B. Output Characteristic

In accordance with the assumptions made, the operation of the converter in the steady-state could be illustrated by the vector diagrams shown in Fig.2. The voltage vectors are shown by a continuous thin line while the current vectors are presented by a continuous thick line. By \dot{U}_{ab} the vector of the first harmonic of the inverter output voltage u_{ab} (between points a and b) is denoted, by \dot{U}_{C2} - the voltage u_{C2} over the capacitor C_2 , and by \dot{U}_{LC1} - the voltage u_{LC1} over the inductor L and capacitor C_1 (between points a and c). By \dot{I}_1 , \dot{I}_2 and \dot{I}_3 the vectors of the currents i_1 , i_2 , and i_3 are denoted, as shown in Figure 1. Angle φ indicates the phase shift of the current i_1 with respect to the voltage u_{ab} , and angle α - the phase shift between the voltages u_{C2} and u_{ab} .

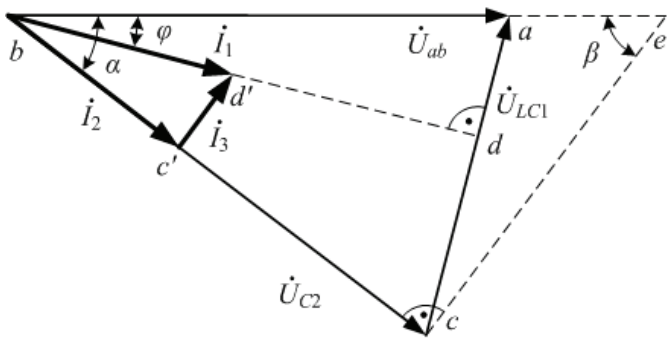


Fig.2. Vector diagrams of the voltages and currents

To better visualize the analysis results in Fig.2 additional constructions are made (shown by means of a dotted line).

Using vector diagrams a system of equations describing the processes in the resonant circuit can be worked out:

$$\begin{cases} \dot{I}_1 = \dot{I}_2 + \dot{I}_3 \\ \dot{U}_{ab} = \dot{U}_{C2} + (Z_L + Z_{C1})\dot{I}_1 \\ \dot{U}_{C2} = Z_{C2}\dot{I}_3 \end{cases} \quad (4)$$

After solution of the system an equation is obtained for the output circuit of the resonant inverter:

$$\dot{U}_{ab} = \frac{\dot{U}_{C2}(Z_L + Z_{C1} + Z_{C2}) + (Z_L Z_{C2} + Z_{C1} Z_{C2})\dot{I}_2}{Z_{C2}} \quad (5)$$

The operation of the power switches ($S_1 \div S_4$) and their antiparallel diodes ($D_1 \div D_4$) results in the rectangular shape of the voltage u_{ab} , its amplitude being equal to the supply voltage U_d . When the rectifier operates in a continuous current mode, the voltage u_{C2} also has a rectangular shape and its amplitude is equal to the output voltage U_0 . Then the modules of the vectors of the first harmonics of u_{ab} , u_{C2} and i_2 are defined as:

$$U_{ab} = \frac{\sqrt{8}}{\pi} U_d \quad U_{C2} = \frac{\sqrt{8}}{\pi} U_0 \quad I_2 = \frac{\pi}{\sqrt{8}} I_0 \quad (6)$$

Substitution of (3) and (6) in (5) and certain formal transformations lead to:

$$\frac{8}{\pi^2} U_d^2 = \left[\frac{\sqrt{8}(1+a-av^2)}{\pi} \right]^2 U_0^2 + \left[\frac{\pi(1-v^2)}{\sqrt{8}v} \rho_0 \right]^2 I_0^2 \quad (7)$$

For obtaining summarized results all quantities are normalized as follows: voltages with respect to U_d ; and currents with respect to U_d/ρ_0 . Thus, from equation (7) the expression of the normalized output characteristic of the converter is obtained:

$$1 = (1+a-av^2)^2 U_0'^2 + \frac{\pi^4(1-v^2)^2}{64v^2} I_0'^2 \quad (8)$$

The above expression is an equation of an ellipse with semi-axes defined by the voltage of a no-load and the current of a short circuit:

$$U_{0\max}' = \frac{1}{1+a-av^2} \quad I_{0\max}' = \frac{8v}{\pi^2(v^2-1)} \quad (9)$$

Equation (8) shows that under certain conditions it is possible for the converter to behave like an ideal source of current. For this purpose it is necessary for the frequency distraction to obtain a limiting value:

$$v_{\text{bound}} = \sqrt{\frac{a+1}{a}} \quad (10)$$

Then the output current is determined by the expression:

$$I_{0\text{bound}}' = \frac{8}{\pi^2} \sqrt{a(a+1)} \quad (11)$$

The above equation shows that this value of the current depends only on the parameter a .

From (8), the dependence of the output voltage on the output current in relative units is easily obtained:

$$U'_0 = \sqrt{\frac{v^2 - \left[\left(\frac{\pi^2}{8} \right) (v^2 - 1) \right]^2 I_0'^2}{(v + av - av^3)^2}}. \quad (12)$$

In accordance with the above equation, a family of output characteristics obtained at $a = 1$ and at different values of the frequency detuning $v \geq 1$ is presented in Fig.3. With the exception of the boundary characteristic obtained at $v = v_{bound}$, all the other characteristics are arcs from ellipses. It can be seen that the characteristics obtained at $v > v_{bound}$ are concentric to each other and for them the no-load voltage and the short circuit current decrease with the increase of the operating frequency. The characteristics obtained at $v < v_{bound}$ intersect. In this case with the increase of the operating frequency the short circuit current decreases, but the no-load voltage increases.

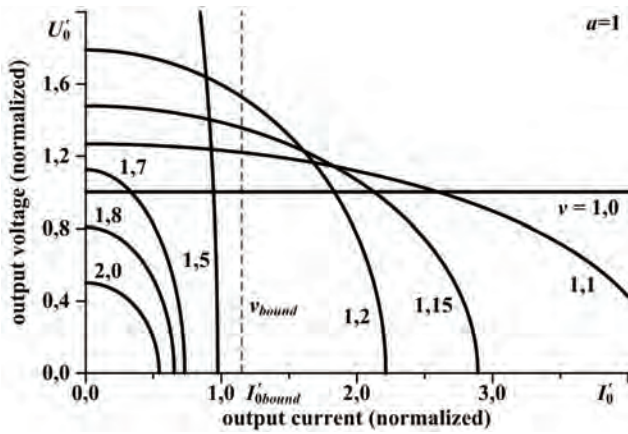


Fig.3. Output characteristics of the converter

Fig.3 shows that in the plan of the output characteristics an operating area in which $U'_0 > 1$ exist. Therefore, under certain conditions the output voltage is higher than the supplied one. This effect is observed for all characteristics obtained at $1 < v \leq v_{bound}$. The output voltage can be higher than the supplied one at $v > v_{bound}$.

The normalized dependence of the output power alteration on the output current is obtained from equation (12):

$$P'_0 = I'_0 U'_0 = I'_0 \sqrt{\frac{v^2 - \left[\left(\frac{\pi^2}{8} \right) (v^2 - 1) \right]^2 I_0'^2}{(v + av - av^3)^2}}. \quad (13)$$

The study of the above expression shows the maximum value of the power:

$$P'_{0max} = \sqrt{\frac{16v^2}{\pi^4 (v^2 - 1)^2 (1 + a - av^2)^2}}, \quad (14)$$

which could be obtained when:

$$\begin{aligned} U'_{0(P_{max})} &= 1 / \left[\sqrt{2} (1 + a - av^2) \right] \\ I'_{0(P_{max})} &= \sqrt{32} v / \left[\pi^2 (v^2 - 1) \right]. \end{aligned} \quad (15)$$

A family of dependencies of the output power on the output current obtained when $a=1$ and with different values of the frequency detuning $v \geq 1$ is presented on figure 4.

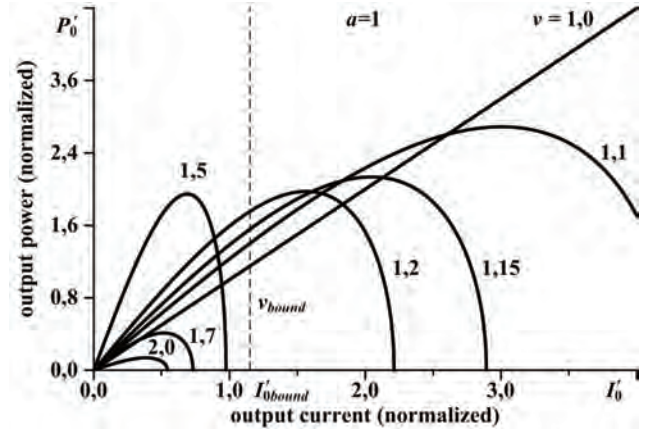


Fig.4. Dependencies of output power P_0 on the output current I_0

It is obvious that the characteristics have a clearly expressed maximum, the position of which shifts towards the beginning of the coordinates with the increase of the frequency detuning.

C. Conditions of soft commutation

As mentioned earlier, an important feature of the resonant converter operation is the mechanism of commutation of the inverter power switches. This is related, on the one hand, to the way of controlling the power switches, and on the other hand, to the type of auxiliary commutation circuits. The required commutation mechanism can be determined based on the phase shift φ between the inverter voltage u_{ab} and the inverter current (in this case i_l). When the voltage is ahead of the current the controllable switches commutate at zero voltage, i.e., zero voltage switching (ZVS) occurs. Otherwise, zero current switching takes place (ZCS). The angle φ can be determined based on the vector diagrams in Fig.2. Since the current i_l and the voltage u_{LC1} are shifted with respect to each other at an angle of 90° , the segment bd is a height in the triangle abc . Its length is easily expressed by the lengths of the sides of Δabc . Then the same angle φ can be expressed by the lengths of the sides in the rectangular triangle abd . Before that, however, the length of the side ac has to be found (voltage u_{LC1}). This is possible by applying the cosine theorem. To find the phase shift α between the vectors of the voltages u_{ab} and u_{C2} equation (5) is used. Substitution of (3) and (6) leads to:

$$\alpha = \arctg \left[\frac{\pi^2}{8} \frac{I'_0 (1 - v^2)}{U'_0 (-av^3 + av + v)} \right]. \quad (16)$$

Then the final expression for the angle φ has the form:

$$\varphi = \arctg\left(\frac{8v^2a + \pi^2 V_0' \sin \alpha}{\pi^2 V_0' \cos \alpha}\right). \quad (17)$$

Fig.5 presents dependencies of the angle φ on the output current I_0 , obtained when $a=1$ for different values of the frequency distraction $v \geq 1$. It can be seen that when $v > v_{bound}$ the angle φ always has a positive value. Consequently, the inverter voltage u_{ab} is ahead of the inverter current i_1 and the controllable switches commute at zero voltage. When $v < v_{bound}$, with the increase in the output current I_0 from 0 to I_{0max} the angle φ changes from -90° до $+90^\circ$. It means that whereas at heavy loads the controllable switches commute at zero voltage, at small loads switching is accomplished at zero current.

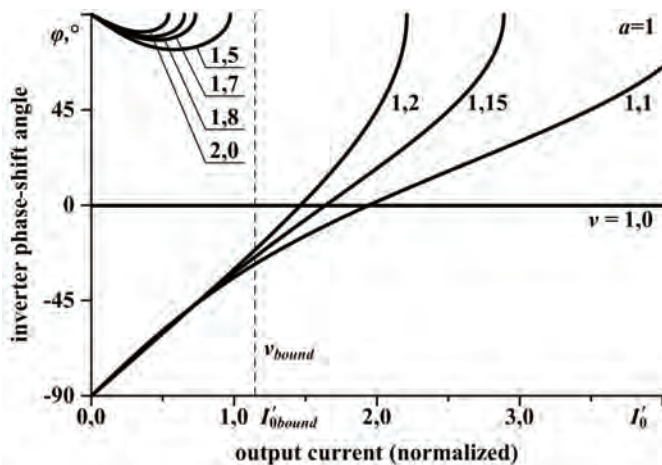


Fig.5. Dependencies of phase-shift angle φ on the output current I_0

During the process of operation while retaining the conditions for soft commutation, the commutation mechanisms of the controllable switches cannot be easily changed. Therefore, if it is necessary to use ZCS, the range of load should be limited to the modes, close to a no-load, and the frequency distraction should not exceed v_{bound} . When the control devices have been chosen to switch at zero voltage the converter can operate in the whole range from a no-load to a short circuit. To maintain the conditions for soft commutation, however, the frequency distraction in the area of small loads should be higher than v_{bound} .

III. CONCLUSION

A study of LLC resonant DC-DC converter with capacitive filter in the load circuit is accomplished by means of harmonic analysis, taking into account the influence of the first harmonic only. It is assumed in the process of consideration that the converter operates with a higher frequency than the basic resonant frequency of the resonant circuit.

As a result of the analysis an expression for the output characteristic of the converter is obtained. Dependencies of characteristic angles of the phase shift between main quantities for the converter circuit under consideration are determined.

Based on the obtained results the type of the output characteristics of the LLC converters is defined. It is shown that under certain conditions these converters behave like an ideal current source without using any additional resources (e.g., as with converters with second order resonant circuits, where current feedback is used). This mode is possible at a precisely defined frequency distraction that depends only on the values of the capacitors in the resonant circuit. It is established that the output voltage of the converter may have a higher value than the supply voltage. Some of the conditions under which this effect is observed are discussed.

It is established from the dependencies of the angle of phase shift between the voltage and the current of the inverter what the commutation mechanisms of the controllable converter device should be. Although the operating frequency is higher than the basic resonant frequency of the resonant circuit, from small loads the switches are supposed to commute at zero current, i.e., zero current switching occurs. This effect is observed at frequency distraction lower than that at which the converter behaves like a current source. In the rest of the cases, the controllable devices should switch at zero voltage. The transition from ZVS to ZCS is mainly due to the change of the load and can be avoided by changing the operating frequency.

The study results can be used in designing LLC converters used as power supplies of electric arc welders, fluorescent lamps, lasers, etc.

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