

Pspice Simulation of Optoelectronic Circuits of Detectors

Hristo Sabev¹ and Tsanko Karadzhov²

Abstract – The operation of circuits of ionizing radiation detectors, implemented by means of PIN photodiodes, is simulated. Semiconductor optoelectronic detectors of ionizing radiation- silicon and germanium are examined. Two electric circuits for temperature compensation in the performance of optoelectronic detectors of ionizing radiation at low energy levels of ionizing particles in fast and slow changes in temperature are proposed. Simulation of the output voltage of the optoelectronic detectors is implemented.

Keywords – Ionizing radiations, PIN photodiodes, PSPICE simulations.

I. INTRODUCTION

Standards for optoelectronic detectors of ionizing radiations are given in [1, 2, 3]. The optoelectronic detectors of ionizing radiations, including PIN photodiodes, are viewed in [4, 5, 6, 7, 8, 10, 11]. The operation of three circuits of ionizing radiation detectors, realized by means of PIN photodiodes, are simulated. They are considered in [9].

II. OPTOELECTRONIC CIRCUITS OF DETECTORS

Figure 1 shows a circuit where the PIN photodiode is connected to an AC current circuit. The circuit is used for registering ionizing radiations. The photodiode operates under inverse voltage in photodiode mode. The amplifier DA1 has a high input resistance. A silicon PIN photodiode SFH 520 of large area is used [11].

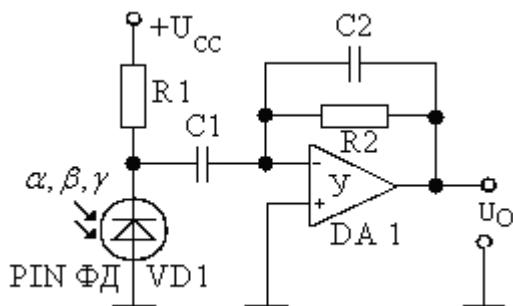


Fig. 1. Circuit where the PIN photodiode is connected

Within the next circuits an operating photodiode (VD1) and

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a compensating photodiode (VD2) are used.

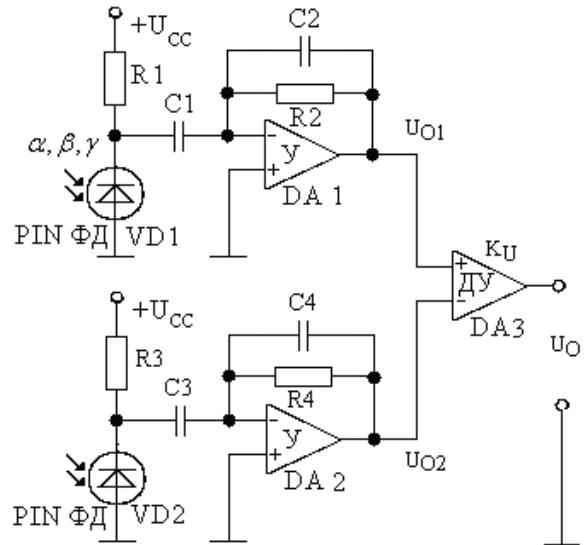


Fig. 2. A detector circuit with compensation of fast temperature changes

The output circuit voltage is:

$$U_o = K_U \cdot I_{ph1} \cdot R_2 \quad (1)$$

$$U_o = K_U \cdot (U_{o1} - U_{o2}) = K_U \cdot [(I_{ph1} + I_{D1}) \cdot R_2 - I_{D2} \cdot R_2] = K_U \cdot I_{ph1} \cdot R_2 \quad (2)$$

When the photodiodes are identical, the two dark currents are equal $I_{D1} = I_{D2}$

where K_U - coefficient of amplification according to the voltage of the differential amplifier DA3, I_{ph1} - photocurrent of the PIN photodiode 1, I_{D1} , I_{D2} - dark currents of the PIN photodiodes 1 and 2. The following conditions must be satisfied:

$$R1 = R3; R2 = R4; C1 = C3; C2 = C4; I_{ph2} = 0 \quad (3)$$

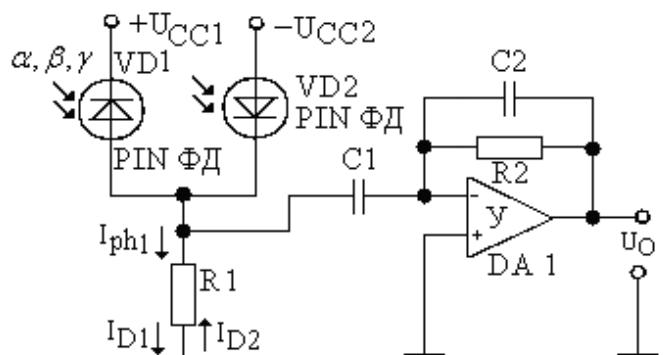


Fig. 3. A detector circuit with compensation of slow temperature changes

The supply voltages of the circuit are equal $U_{CC1} = U_{CC2}$

The voltage on the resistor R1 is:

$$U_{R1} = [I_{ph1} + (I_{D1} - I_{D2})] \cdot R_1 = I_{ph1} \cdot R_1$$

$$I_{D1} = I_{D2}; I_{ph2} = 0 \quad (4)$$

For the circuits in fig. 2 and fig. 3 differential detectors or coordinate (position)-sensitive detectors (PSD) should be used.

For an operation in a wide temperature range the photodiodes of ORTEC company should be used [10]. ORTEC produces photon detectors made of high purity germanium (Ge), of a P or N type with a cryostat.

The circuits in fig. 1, 2 and 3 possess a common basic circuit shown in fig. 4.

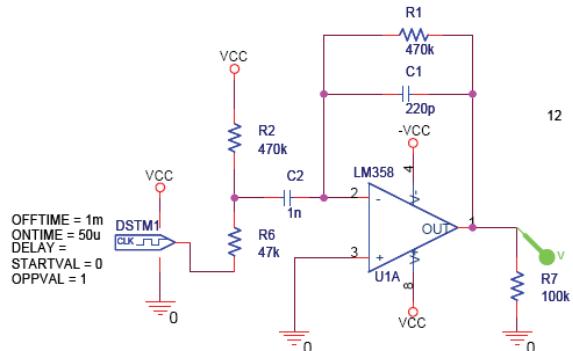


Fig. 4. Basic circuit for PSPICE simulations of optoelectronic detectors

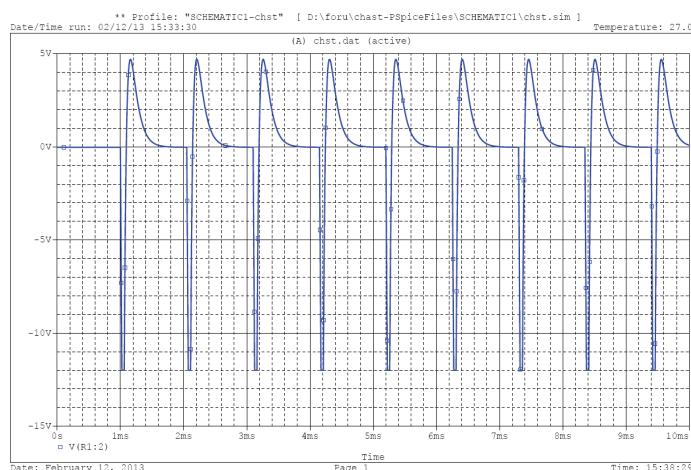


Fig. 4.a. Output voltage of the circuit in fig. 4.

III. CONCLUSION

Circuits of optoelectronic detectors of ionizing radiation, implemented by means of PIN photodiodes, are simulated. The results from the simulations are compared with those obtained from the calculations and measurements done.

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