

Sputtering of Thin Films on Flexible Substrates

Pavlik Rahnev¹, Silvija Letskovska², Dimitar Parachkevov¹ and Kamen Seymenliyski²

Abstract – The aim of this work is to investigate the properties of thin film deposited on flexible substrates. The main instrument for experiment is high rate magnetron sputtering. For this purpose high vacuum modified installation B55 (Hoch Vacuum Dresden) is used. As substrates different kind of no organic (metal foil) and organic materials are used. The main parameters of films (resistive, semiconductor, dielectric) are described as deposited and after thermal treatment. The comparison between electrical parameters and composition, structure is done. The main applications of such thin films are in photo voltaic and thin film displays.

Key words – thin films, sputtering, flexible substrates, organic substrates.

I. INTRODUCTION

In the last years there is an increasing interest of using flexible substrates in electronics. Usually the investigations are provided mainly in three directions:

- Materials for substrates [1];
- Technology for deposition of different films [2];
- Circuit application [3];
- Problems appearing in the time of technology processes [4].

The application of flexible substrates is usually in organic light – emitting displays [5, 6], thin film transistors [7-9], sensors [10, 11] and microelectronic modules [12, 13]. The advantages of polymers substrates are mainly in there multiple elastic deformation, easy mechanical shaping, wide range of polymer materials, low cost and est.

The use of polymer materials in integrated circuits is difficult because organic can not be included into the microelectronic processes. Polymers have low temperature stability and low resistance against concentrated chemicals which limited their application in integrated circuits.

It is clear that the choice of polymer substrate is very wide, the combination substrate and deposited films depends on the specific technology - method of deposition, equipment, parameters of the processes.

¹Pavlik Rahnev – Technical College, Assen Zlatarov University, Y. Yakimov Str. 1, Burgas 8010, Bulgaria, E-mail: pavlikrahnev@abv.bg

²Silvija Letskovska - Burgas Free University, San Stefano 62, 8000 Burgas, Bulgaria, E-mail: silvia@bfu.bg

¹Dimitar Parachkevov – Assen Zlatarov University, Y. Yakimov Str. 1, Burgas 8010, Bulgaria, E-mail: parachkevov@abv.bg

²Kamen Seimenliyski –Burgas Free University, San Stefano 62, 8000 Burgas, Bulgaria, E-mail: silvia@bfu.bg

The question in front of this work is to check the combination of different flexible substrates and different thin films deposited in one and same technological cycle as well as the electrical parameters of the films compared with common used quartz substrates.

II. CHOICE OF MATERIALS

2.1. Flexible substrates.

As it was described above there is a wide choice of materials used in the technology – stainless steel (metal), polymers (polyimide, polyester, Teflon, polystirol) and flexible special glass based materials.

The main parameters of the substrates are directly connected with properties of the deposited films and prepared circuits:

- Structure of the films;
- Parasitic capacitance;
- Power dissipation.

TABLE I
SUBSTRATE MATERIAL

Parameter	Stainless steel	Teflon (Du Pont Trade Mark)	Polyimide	Polyester	Sital (CT-50-1)
R_{iz} (ohm)	-	$>10^{10}$	$>10^{10}$	$>10^9$	$>10^9$
ϵ (-)	-	2-3	2-3	2-3	5
λ (W/mK)	30	1.2	1.5	1.5	6
T_{max} ($^{\circ}$ C)	>1000	150	150	150	400

From Table I is seem that polymer materials are good insulators, but with low thermo conductivity.

This will cause problems in device and circuit with high power dissipation.

The sizes of the experimental substrates are as follows;

- Thickness – $0.2 \div 0.5$ mm;
- Linear dimensions – 60×48 mm (2"x2") as a standard for glass and ceramic substrates.

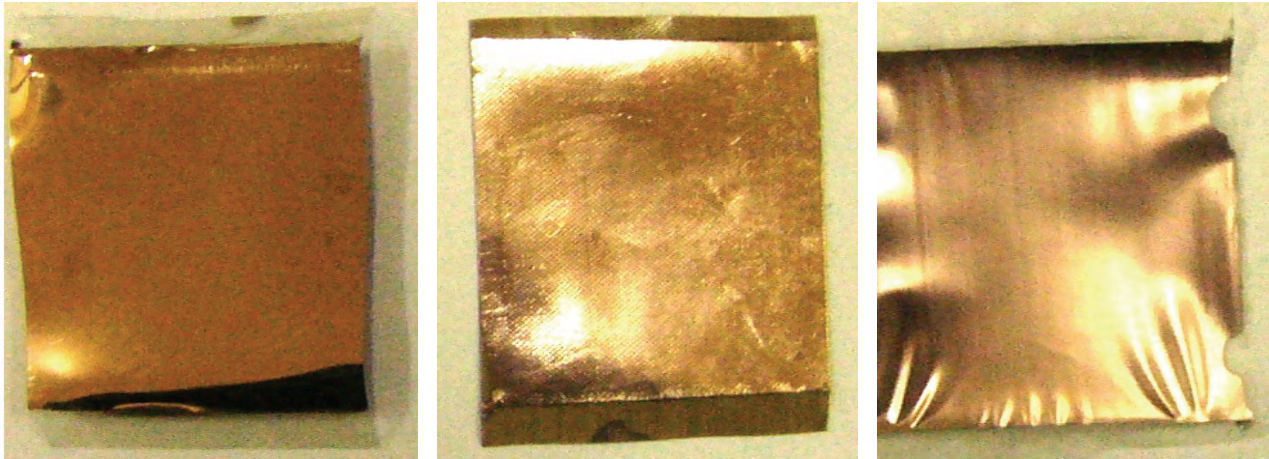


Fig. 1. Organic substrates after thermal evaporation.

2.2. Materials for deposition.

For experimental work four different materials are chosen:

- Cu - as a good conductor;
- NiCr – typical resistive film;
- Si – typical semiconductor;
- SiO₂ – usual dielectric in MOS integrated circuits.

To estimate the parameters of the deposited film sheet resistance (R_s) is used.

III. METHOD OF VACUUM DEPOSITION AND MATERIALS

2.1. Thermal evaporation.

The evaporation in high vacuum is commonly used technology for thin films because it has simplicity and it is well known in practice. In the same time thermal evaporation has several disadvantages and difficulties such as:

- Evaporation of W, Pt, Ta, Mo;
- Evaporation of alloys;
- Evaporation of compounds;
- Deposition on unstable thermally organic substrates (Fig. 1).

From Fig. 1 is clear that some of the polymer materials lose their form, flatness, that is why evaporation is not suitable for some organic materials.

3.2. Sputtering of thin films.

As it is clear from above the only universal method of deposition is sputtering technique.

For the provided experiments the modified high vacuum installation B55 (Hoch Vacuum Dresden) is used (Fig. 2).

The advantages of this machine are:

- Four sputtering targets;
- Rotation of substrates holders;
- Possibility of heating or cooling of substrates.

The main technology parameters are:

- Final vacuum $p < 0.5 \times 10^{-2}$ Pa;
- Argon pressure $p_{Ar} \approx 10 \div 40$ Pa;
- Sputtering voltage 300÷700 V and Power ≈ 100 W.

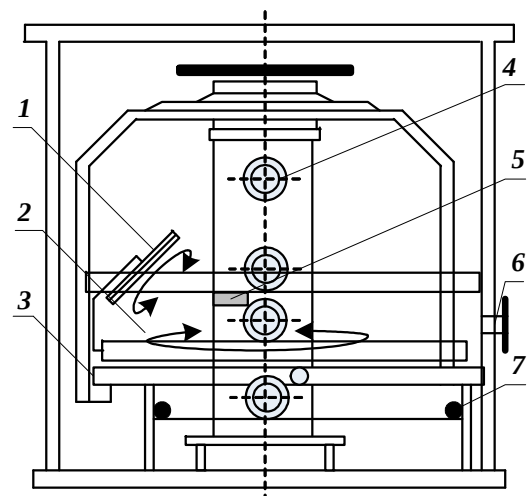


Fig. 2. Vacuum installation B55 with magnetrons.

- 1 - Substrate holder; 2 - Chamber heating/cooling;
3 - Rotating table; 4 - Sputtering target;
5 - Sensor for R_s ; 6 - Vacuum meter;
7 - Infrared heaters.

IV. PREPARATION OF SAMPLES AND RESULTS.

The deposited materials used for the experiments have very wide range of specific resistance.

That is why for estimation sheet resistance $R_s = \rho/d$ is better to be used.

The ratio $n=L/w$ can change from $n=100$ for good conductor (Cu), $n=1$ for NiCr and $n=0.1$ for semiconductor (Si).

The final step in sample preparation is forming of the resistor patterns. For this conventional wet etching or through mask deposition are used.

Finally, the samples have topology and sections are given in Fig.3.

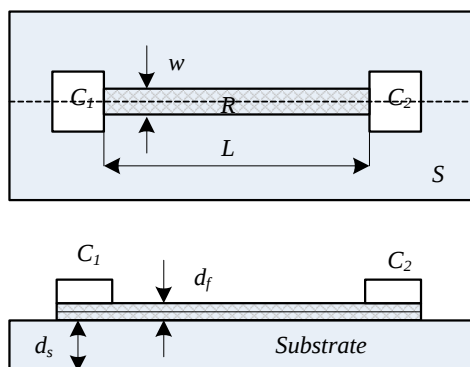


Fig. 3. Topology and cross section of samples.

The results for R_s are shown in Table II. The R_s for different materials are compared with R_s on glass substrates.

TABLE II
COMPARISON OF R_s ON GLASS AND POLYMER SUBSTRATE

Substrate	R_s (omh/sq)		
	Cu	NiCr	Si
Glass	0.10	150	2000
Polymer	0,12	170	3000

It is clear from Table II that sheet resistance on polymers is higher compared with glass substrate. It could be a result of structure changes (defects) and some implantation into the soft organic surface.

REFERENCES

- [1] Flexible Circuit Technology" 3rd Edition by Joseph Fjelstad, BNR Publishing, Seaside OR 2006.
- [2] "Flexible Printed Circuitry" by Thomas Stearns, McGraw-Hill, NY, NY 1995.
- [3] "Handbook of Flexible Circuits" by Ken Gilleo, Van Nostrand Reinhold, NY, NY 1992.

- [4] Pavlik Rahnev, Silvija Letskovska, Dimitar Parachkevov, Problem in using polymer substrates for solar cells, SIELA, 2009.
- [5] J.-W. Kang, W.-I. Jeong, J.-J. Kim, H.-K. Kim, D.-G. Kim, G.-H. Lee, Electrochem. Solid-State Lett. 2007, 10, 75.
- [6] L. Hou, Q. Hou, Y. Mo, J. Peng, Y. Cao, Appl. Phys. Lett. 2005, 87, 243504.
- [7] S. H. Ko, H. Pan, C. P. Grigoropoulos, C. K. Luscombe, J. M. J. Fréchet, D. Poulikakos, Nanotechnology 2007, 18, 345202.
- [8] U. Haas, H. Gold, A. Haase, G. Jakopic, B. Stadlober, Appl. Phys. Lett. 2007, 91, 043511.
- [9] A. L. Briseno, S. C. B. Mannsfeld, M. M. Ling, S. Liu, R. J. Tseng, C. Reese, M. E. Roberts, Y. Yang, F. Wudl, Z. Bao, Nature 2006, 444, 913.
- [10] V. Shamanna, S. Das, Z. Celik-Butler, K. L. Lawrence, J. Micromech. Microeng. 2006, 16, 1984.
- [11] F. Jiang, G.-B. Lee, Y.-C. Tai, C.-M. Ho, Sens. Actuators A 2000, 79, 194.
- [12] S. A. Dayeh, D. P. Butler, Z. Celik-Butler, Sens. Actuators A 2004, 118, 49.
- [13] S. Tung, S. R. Witherspoon, L. A. Roe, A. Silano, D. P. Maynard, N. Ferraro, Smart Mater. Struct. 2001, 10, 1230.