

# Algorithmic Application for Calculation of Chopping Currents and High Transient over-Voltages for a New Vacuum Interrupter

Medium Voltage Switching System

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*Abstract* - Since many years up to date now, most of researchers demonstrate the development of the vacuum interrupters by using of a refractory materials "Alloys" for sustainable against of an arcing currents and both of chopping currents and transient over voltages. The conventional designing switches are still constructing in the same classical steps of atoggle ways, thus have been dominated yet.

This paper presents the sequence of the switching process time for a static switching process for application of medium voltage switching technique. Since DESY & Behlke Power Electronics and LLC are designed for RF systems, basically depending on Crow-bar and snubber circuit for protection applications.

*Keywords* - Calculating chopping currents, Damping circuit, LTT thyrsitors, Sequences of switching times, MATLAB/Simulink.

#### I. INTRODUCTION

Circuit breakers using different media for interrupting currents may exhibitdifferent characteristics with respect to all of the primary functional requirements of the breaker. Each type of circuit breaker has a unique set of characteristics which must be thoroughly understood before the breaker can be applied with correctly designing and confidence by considerations of arcing currents, chopping currents, over voltage transients, multiple re-ignitions, voltage escalation and high frequency which calculate for full load during processing switching time.

Both of hybrid application and damping circuit theory have been offered a smooth process switches which consists of high speed mechanical contact with power semiconductors as diverting switching, itsan excellent performance in limiting chopping current. In hybrid switchsystem, the mechanical contact conducts large current with lowlosses, [1], [2], and [10].

#### II. ANALYSING DAMPING CIRCUIT

2.1 The vacuum interrupter involves very large number of practical transients problems in power system of medium voltage switching process

The analysis shows that the effect of damping an oscillatory circuit can be described in terms of a single parameter, designed  $\eta$ , or its reciprocal  $\lambda$ , which is the ratio

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of the resistance to the surge impedance of the circuit i.e;

$$\eta = \frac{R}{Z} = \frac{R}{\sqrt{L/C}} \tag{1}$$

This fact permits the construction of generalized damping curves for different values of  $\eta$ , from which the solutions to many practical problems can be extracted with about the same effort as one expand by using a table of logarithms.

Normally our analysis for the switching process basically on parallel *RLC* circuit as following;

- *L* indicative load of stator winding coils;
- *C* parallel parasite`s capacitors;
- *R* evaluating resistance that can be damping oscillating.

$$i_{L}(s) = \frac{V(0)}{L} \cdot \frac{1}{s^{2} + \frac{s}{T_{p}} + \frac{1}{T^{2}}}$$
(2)

$$L^{-1} \frac{1}{s^2 + \frac{s}{T_{\star}} + \frac{1}{T^2}}$$
(3)

From Eq. (3) express in dimensionless from the current in the inductor of any parallel *RLC* circuit, with any degree of damping. We note that the only parameter involved is  $\eta$ . So that a family of generalized curves can be drawn from Eq. (3) for different values of  $\eta$  with dimensionless quantity, t', as abscissa. This has been done in Fig. 1.



Fig. 1. Generalized plot of inverse transform

Where  $\eta = 0.5$ , the sine function changes from a circular to a hyperbolic function. We might have developed the curves for this condition, following the same argument. By calculating the inductor current in parallel *RLC* circuit under conditions

of a subsidence transient, but have a far wider application . However to gain familiarity with these curves, consider a specific example where the inductor current is required in a circuit in which the components have the following values:  $R = 10^5 \Omega$ , L = 5 H,  $C = 2 \cdot 10^{-8}$  F.

These values are typical of unloaded transformer, where R represents the equivalent loss resistance. Suppose

 $Vo = 13.8\sqrt{2}$  KV So.  $Zo = \sqrt{L/C} = 5 \cdot 10^4 \Omega$ ,  $\eta = 2$ . The curve labelled  $\eta=2$  in Fig. 3 gives the shape of the current we are looking for.

2.2 Parallel switching technique: the resistor application technique R circuit will be diminished the amplitude of the first peak value of chopping current, if we connect this circuit as parallel application with two electrodes – parallel operation mode [1]

The flexible rates for transition of du/dt and di/dt between to elements such as electroplates and set of thyristors. Flexible du/dt and di/dt [6]-[9] for the set of thyrsitors as a control techniques are presented in Simulink models. It propos to make a diverting of the chopping currents after the time of ionization by using a damping resistance circuit



Fig. 2. di / dt and du / dt alignment between electroplates and thyristor

### **III.** SWITCHING TRANSITION RATES

Vacuum interrupter involves by switching transition rates of di/dt and du/dt conversion. An Experiment synthetic figure which describes the test circuit an arc of a few thousand amperes is drawn between a pair of separating contact *"synthetic prototype"* with a sealed vacuum interrupter.

Current commutation is typically achieved within 20 us. The circuit subsequent applies a fast-rising transient recovery voltage across the gap. by adjusting circuit parameters it is possible to cause the switch to re-ignite. A mathematical model which analyzes events during the commutation period and the recovery period afterwards has been proposed by Childs and Greenwood [8]. Two time periods are defined in the mathematical model: a commutating period prior to current zero and post arc time. The model has been used to calculate the post arc current and transitions time di / dt and du / dt were calculated according to following formula

$$di / dt = I_{ss} / t_{r}$$
(4)



Fig. 4. Transition process di/dt and du/dt for TRV

The vacuum switch under test VS has its contacts open at short fixed gap; the gap selected was in the range (100-150mm). The capacitor C1 is charged until the gap of VS sparks over , at which time C1 delivers an oscillatory current to the gap. The fixed gap assures a reasonably constant breakdown voltage (~ 15KV). dI/dt is controlled by L1. The TRV frequency is adjusted by C2 (<<C1) as it resonates with L1. The ranges of values chosen during the course of the test were;  $I_{ss} = 1.0$ KA, 2.0KA, 3.0KA, 4.0KA & 5.0KA. The twenty nine shots were made to calculate di/dt and du/dt with several test were for each ramp down time; the results are summarized in Table I.

 TABLE I

 CALCULATION OF MECHANICAL SWITCHING TRANSITION RATES

Iss	tr	di/dt	Q	du/dt	Ipk	V2
KA	μs	A/ μs	-		•	KV
1.0	7.8	128	9.4	1.2E10	22	2.0
1.0	6.9	145	18.2	1.2E10	30	2.25
1.0	6.13	163	21.3	1.2E10	38	2.5
1.0	5.06	197	28.3	1.2E10	44	3.0
1.0	4.32	232	38.3	1.2E10	60	3.5
1.0	3.77	266	49.7	1.2E10	67	4.0
1.0	3.34	299	49.7	1.2E10	67	4.5
2.0	10.9	183	17.1	1.9E10	32.5	3.0
2.0	9.1	221	29.3	1.9E10	44.5	3.5
2.0	7.8	256	42.8	1.9E10	53.0	4.0
2.0	6.9	291	57.0	1.9E10	67.0	4.5
2.0	6.13	326	67.0	1.9E10	75.5	5.0
2.0	5.55	361	80.0	1.9E10	80.0	5.5
3.0	12.6	237	27.5	1.2E10	40.75	4.0
3.0	11.5	261	43.0	1.2E10	54.3	4.3
3.0	10.5	275	48.0	1.2E10	63.0	4.5
3.0	10.1	295	34.5	1.2E10	68.0	4.75
4.0	16.3	245	15.5	1.2E10	30.0	6.0
4.0	14.9	268	18.5	1.2E10	36.5	6.0

 $I_{SS}$  - the steady state current being commutated,

tr - the ramp down us,

dI/dt - the average rate of current decline during current commutation as given by Iss/tr,

Q - the average integrated post- arc current,

du / dt - the average rate of rise of recovery voltage, being computed from the slope of the linear portion of the re-covery voltage,

I peak - arc current,

V2 - charging voltage on the commutating capacitor.

The results we have obtain that are the sequence of test with progressively ramp down time as Fig. 6. The transition rates of du / dt is approximately constant **1.2E10** V/s, because that explains the materials Alloys (CU-CR-70) is a constant transition rate but the transition rates of dI / dt is changed for average values (128 A/us up to 268 A/us for the  $I_{SS}$  5.0KA). This indicates both of the similarity functional with powerelectronics transition rates and Logical consistency for my currently research with Simulink experiments also.

# IV. FLOW CHART & SINGLE LINE DIAGRAM

Flexible rates among electroplates and sets of thyristorsare a new control techniques to achieve

1) independent control of the out put terminals between two components du/dt and di/dt rates that adjusted *electronically* over a wide range without electromagnetic interference EMI and voltage overshoots and

2) maximum compatibility with integrated M.V circuit techniques



Fig. 5. Algorithmic application for turned ON process of parallel operation mode



Fig. 6. Algorithmic switching times for turned -ON process



Fig. 7. Algorithmic switching times for turned - OFF process



Fig. 8. A single line diagram of a compact design of a new proposal of vacuum interrupter for medium voltage switching

# **IV. CONCLUSIONS**

- 1. The static capacitors are always interactive with the switching process Inherited capacitors for each process unit function.
- Many researching papiers indicate that the chopping current will not increase more that 5A – first peak value for most full load operation with all types of M.V synchronous motors.
- Further measurement shall be implemented for determine the maximum values of discharged static capacitance.
- The theory of rectifying chopping currents and dissipated energy thorough the resistor will be offer the static switching process.

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